

### Quantum Data MHL Compliance Test Coverage

Interface	Product Type	Section	Test Description	MHL 1.2/1.3/1.4 - QD support / Eqpt Req'd MHL 1.3/1.4 Approved by MHL Consortium!	MHL 2.0/2.1/2.2 - QD support / Eqpt Req'd Approved by MHL Consortium!
MHL	SOURCE	<b>Electrical Tests</b>			
MHL	SOURCE	3.1.1.1	Electrical Tests	Not Supported	Not Supported
MHL	SOURCE	3.1.1.2		Not Supported	Not Supported
MHL	SOURCE	3.1.1.3		Not Supported	Not Supported
MHL	SOURCE	3.1.1.4		Not Supported	Not Supported
MHL	SOURCE	3.1.1.5		Not Supported	Not Supported
MHL	SOURCE	3.1.1.6		Not Supported	Not Supported
MHL	SOURCE	3.1.1.7		Not Supported	Not Supported
MHL	SOURCE	3.1.1.8		Not Supported	Not Supported
MHL	SOURCE	3.1.1.10		Not Supported	Not Supported
MHL	SOURCE	3.1.1.11		Not Supported	Not Supported
MHL	SOURCE	3.1.1.12	Electrical Tests	Not Supported	Not Supported
MHL	SOURCE	3.1.1.13	Rx Sense Impedance	CBUS Module	CBUS Module
MHL	SOURCE	3.2.2	<b>TMD5 Coding</b>		
MHL	SOURCE	3.2.2.1	Legal Codes Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.2.2	Basic Protocol Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.2.3	Packet Types Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.2.4	Legal Codes Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.2.5	Basic Protocol Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.2.6	Packet Types Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3	<b>Video Modes</b>		
MHL	SOURCE	3.2.3.1	Video Formats Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.2	Required Pixel Encoding Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.3	AVI InfoFrame in Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.4	Video Quantization Ranges	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.5	Video Formats in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.6	Pixel Encoding in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.3.7	AVI InfoFrame in Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.4	<b>Audio Tests</b>		
MHL	SOURCE	3.2.4.1	IEC 60958 / IEC 61937	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.4.2	Audio Clock Regeneration	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.4.3	Audio InfoFrame	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.5	HDCP Test	882EA	882EA
MHL	SOURCE	3.2.6.1	EDID Reading Test	980 CBUS Module	980 CBUS Module
MHL	SOURCE	3.2.6.2	Device Capability Registers Test	980 CBUS Module	980 CBUS Module
MHL	SOURCE	3.2.6.3	Device Status Registers Test Normal Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.6.4	Device Status Registers Test Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.7.1	RCP Sub-command Receiving Tests	980 CBUS Module (all tests)	980 CBUS Module (all tests)
MHL	SOURCE	3.2.7.2	RCP Sub-command Transmitting Tests	980 CBUS Module (all tests)	980 CBUS Module (all tests)

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MHL	SOURCE	3.2.8	RAP Sub-command Tests	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.9.1	3D Video Mode Support (3D REQ)		980 CBUS Module
MHL	SOURCE	3.2.9.2	3D Video Format Timings in Normal Mode		Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.9.3	3D Video Mode Indicator		Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.9.4	3D Video Format Timings in Packed Pixel Mode		Protocol Analyzer module w/TPA
MHL	SOURCE	3.2.10.1	UCP Sub-Command Receiving Tests		980 CBUS Module (all tests in section)
MHL	SOURCE	3.2.10.2	UCP Sub-Command Transmitting Tests		980 CBUS Module (all tests in section)
MHL	SOURCE	3.3	<b>CBUS Tests</b>		
MHL	SOURCE	3.3.1	CBUS Source DUT Common Methodologies	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.3	Link Layer Electrical – Source: Absolute Maximum Voltages	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.4	Link Layer Timing – Source DUT Output: Pre-Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.5	Link Layer Electrical – Source DUT Output: Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.6	Link Layer Timing – Source DUT Output: Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.7	Link Layer Electrical – Source DUT Output: Arbitration/Sync/Data Signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.8	Link Layer Timing – Source DUT Output: Arbitration/Sync/Data in Nanoseconds	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.9	Link Layer Timing – Source DUT Output: Arbitration/Sync/Data in Bit Times	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.10	Link Layer Timing – Source DUT Output: Link-Level NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.11	Link Layer Timing – Source DUT Output: ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.12	Link Layer Timing – Source DUT Output: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.13	Link Layer Behavior – Source DUT Output: Ill-formed packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.14	Link Layer Timing – Source DUT Input: Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.15	Link Layer Electrical – Source DUT Input: Arbitration/Sync/Data signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.16	Link Layer Timing – Source DUT Input: Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.17	Link Layer Timing – Source DUT Input: Data	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.18	Link Layer Timing – Source DUT Input: NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.19	Link Layer Timing – Source DUT Input: ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.20	Link Layer Timing – Source DUT Input: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.21	Link Layer Behavior – Source DUT Input: Ill-formed packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.22	Link Layer Timing – Source DUT Input: Disconnect	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SOURCE	3.3.23	Link Layer Electrical – Source DUT VBUS Control	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.1.1.1	Electrical Tests	Not Supported	Not Supported
MHL	SINK	4.1.1.2		Not Supported	Not Supported
MHL	SINK	4.1.1.3		Not Supported	Not Supported
MHL	SINK	4.1.1.4		Not Supported	Not Supported
MHL	SINK	4.1.1.5		Not Supported	Not Supported
MHL	SINK	4.1.1.6	Electrical Tests	Not Supported	Not Supported
MHL	SINK	4.1.1.7	Rx Sense Impedance	980 CBUS Module	980 CBUS Module
MHL	SINK	4.1.1.8	Electrical Tests	Not Supported	Not Supported
MHL	SINK	4.1.1.10		Not Supported	Not Supported

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MHL	SINK	4.1.1.11		Not Supported	Not Supported
MHL	SINK	4.1.1.12	Electrical Tests	Not Supported	Not Supported
MHL	SINK	4.2.2	<b>TMDS Coding</b>		
MHL	SINK	4.2.1.1	Character Synchronization Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	SINK	4.2.1.2	Packet Types Normal Mode	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.1.3	Character Synchronization Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SINK	4.2.1.4	Packet Types Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.2	<b>Video Tests</b>		
MHL	SINK	4.2.2.1	Video Formats in Normal Mode	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.2.2	Pixel Encoding Normal Mode	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.2.3	Video Quantization Ranges	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.2.4	Video Formats in Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.2.5	Pixel Encoding Packed Pixel Mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.3	<b>Audio Tests</b>		
MHL	SINK	4.2.3.1	IEC 60958 / IEC 61937	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.3.2	Audio Clock Regeneration	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	SINK	4.2.4	HDCP Test	882EA	882EA
MHL	SINK	4.2.5.1	EDID Test	980 CBUS Module	980 CBUS Module
MHL	SINK	4.2.5.2	Device Capability Register Test	980 CBUS Module	980 CBUS Module
MHL	SINK	4.2.5.3	Device Status Registers Test	980 CBUS Module (1.3 only)	980 CBUS Module
MHL	SINK	4.2.6.1	RCP Sub-command Tests	980 CBUS Module (all tests)	980 CBUS Module (all tests in section)
MHL	SINK	4.2.6.2	RCP Sub-command Receiving Tests	980 CBUS Module (all tests)	980 CBUS Module (all tests in section)
MHL	SINK	4.2.7.1	RAP Sub-Commands Transmitting Test	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	SINK	4.2.8.1	3D Video Mode Support Data		980 CBUS Module (all tests)
MHL	SINK	4.2.8.2	3D Video Format in Normal Mode		Protocol Analyzer module w/TPA
MHL	SINK	4.2.8.3	3D Video Format in Packed Pixel Mode		Protocol Analyzer module w/TPA
MHL	SINK	4.2.9.1	UCP Sub-Command Receiving Tests		980 CBUS Module (all tests in section)
MHL	SINK	4.2.9.2	UCP Sub-Command Transmitting Tests		980 CBUS Module (all tests in section)
MHL	SINK	4.3.3	Link Layer Electrical – Sink: Absolute Maximum Voltages	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.4	Link Layer Electrical – Sink DUT Output: Standby Discovery Impedance	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.5	Link Layer Timing – Sink DUT Output: Pre-Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.6	Link Layer Electrical – Sink DUT Output: Arbitration/Sync/Data Signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.7	Link Layer Timing – Sink DUT Output: Arbitration/Sync/Data in Nanoseconds	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.8	Link Layer Timing – Sink DUT Output: Arbitration/Sync/Data in Bit Times	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.9	Link Layer Timing – Sink DUT Output: Link Level NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.10	Link Layer Timing – Sink DUT Output: Link Level ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.11	Link Layer Timing – Sink DUT Output: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.12	Link Layer Timing – Sink DUT Output: Ill-formed packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.13	Link Layer Electrical – Sink DUT Input: Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)

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Interface	Product Type	Section	Test Description	MHL 1.2/1.3/1.4 - QD support / Eqpt Req'd MHL 1.3/1.4 Approved by MHL Consortium!	MHL 2.0/2.1/2.2 - QD support / Eqpt Req'd Approved by MHL Consortium!
MHL	SINK	4.3.14	Link Layer Timing – Sink DUT Input: Discovery OK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.15	Link Layer Timing – Sink DUT Input: Discovery Reject	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.16	Link Layer Electrical – Sink DUT Input: Arbitration/Sync/Data Signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.17	Link Layer Timing – Sink DUT Input: Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.18	Link Layer Timing – Sink DUT Input: Data	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.19	Link Layer Timing – Sink DUT Input: NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.20	Link Layer Timing – Sink DUT Input: ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.21	Link Layer Timing – Sink DUT Input: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.22	Link Layer Timing – Sink DUT Input: Ill-formed Packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.23	Link Layer Timing – Sink DUT Input: Disconnect	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.24	Link Layer Electrical – Sink DUT VBUS Output	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	SINK	4.3.25	Link Layer Timing – Sink DUT VBUS Turn On Transition	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.1.1.1	Electrical Tests	Not Supported	Not Supported
MHL	DONGLE	5.1.1.2		Not Supported	Not Supported
MHL	DONGLE	5.1.1.3		Not Supported	Not Supported
MHL	DONGLE	5.1.1.4		Not Supported	Not Supported
MHL	DONGLE	5.1.1.5		Not Supported	Not Supported
MHL	DONGLE	5.1.1.6	Electrical Tests	Not Supported	Not Supported
MHL	DONGLE	5.1.1.7	Rx Sense Impedance	980 CBUS Module	980 CBUS Module
MHL	DONGLE	5.1.1.8	Rx Sense Impedance	980 CBUS Module	980 CBUS Module
MHL	DONGLE	5.1.1.8	Electrical Tests	Not Supported	Not Supported
MHL	DONGLE	5.1.1.10		Not Supported	Not Supported
MHL	DONGLE	5.1.1.11		Not Supported	Not Supported
MHL	DONGLE	5.1.1.12	Electrical Tests	Not Supported	Not Supported
MHL	DONGLE	5.2.1	<b>TMDS Coding</b>		
MHL	DONGLE	5.2.1.1	Character Synchronization Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.1.2	Packet Types in Normal Mode	Protocol Analyzer module w/TPA	Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.1.3	Character Synchronization in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.1.4	Packet Types in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.2	<b>Video Tests</b>		
MHL	DONGLE	5.2.2.1	Video Formats in Normal Mode	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.2.2	Pixel Encoding in Normal Mode	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.2.3	Video Quantization Ranges	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.2.4	Video Formats in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.2.5	Pixel Encoding in Packed Pixel mode	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.3	<b>Audio Tests</b>		
MHL	DONGLE	5.2.3.1	IEC 60958 / IEC 61937	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.3.2	Audio Clock Regeneration	Protocol Analyzer module w/TPA & 882E	Protocol Analyzer module w/TPA & 882E
MHL	DONGLE	5.2.4	HDCP Test	882EA	882EA

### Quantum Data MHL Compliance Test Coverage

Interface	Product Type	Section	Test Description	MHL 1.2/1.3/1.4 - QD support / Eqpt Req'd MHL 1.3/1.4 Approved by MHL Consortium!	MHL 2.0/2.1/2.2 - QD support / Eqpt Req'd Approved by MHL Consortium!
MHL	DONGLE	5.2.5.1	EDID Test	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.2.5.2	Device Capability Register Test	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.2.5.3	Device Status Registers Test	980 CBUS Module (1.3 only)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.2.6.1	RCP Sub-command Receiving Tests	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.2.6.2	RCP Sub-command Transmitting Tests	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.2.7.1	RAP Sub-Commands Test	Protocol Analyzer module w/TPA (1.3 only)	Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.8.1	3D Video Mode Support Data		980 CBUS Module (all tests)
MHL	DONGLE	5.2.8.2	3D Video Format in Normal Mode		Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.8.2	3D Video Format in Packed Pixel Mode		Protocol Analyzer module w/TPA
MHL	DONGLE	5.2.9	UCP Sub-Command Tests		980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.3	Link Layer Electrical – DUT Absolute Maximum Voltages	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.4	Link Layer Electrical – DUT Output: Standby Discovery Impedance	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.5	Link Layer Timing – DUT Output: Pre-Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.6	Link Layer Electrical – DUT Output: Arbitration/Sync/Data Signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.7	Link Layer Timing – DUT Output: Arbitration/Sync/Data in Nanoseconds	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.8	Link Layer Timing – DUT Output: Arbitration/Sync/Data in Bit Times	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.9	Link Layer Timing – DUT Output: Link Level NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.10	Link Layer Timing – DUT Output: Link Level ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.11	Link Layer Timing – DUT Output: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.12	Link Layer Timing – DUT Output: Ill-formed packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.13	Link Layer Electrical – DUT Input: Discovery	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.14	Link Layer Timing – DUT Input: Discovery OK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.15	Link Layer Timing – Dongle DUT Input: Discovery Reject	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.16	Link Layer Electrical – DUT Input: Arbitration/Sync/Data Signaling	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.17	Link Layer Timing – DUT Input: Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.18	Link Layer Timing – DUT Input: Data	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.19	Link Layer Timing – DUT Input: NACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.20	Link Layer Timing – DUT Input: ACK	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.21	Link Layer Timing – DUT Input: Bus Re-Arbitration	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.22	Link Layer Timing – DUT Input: Ill-formed Packets	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.23	Link Layer Timing – DUT Input: Disconnect	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.24	Link Layer Electrical – DUT VBUS Output	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.25	Link Layer Electrical – DUT VBUS Input	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	DONGLE	5.3.26	Link Layer Timing – DUT VBUS Turn On Transition	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.1	MSC – DUT Input: Device Register Space Contents; Reads	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.2	MSC – DUT Output: Vendor-specific and Reserved Header Values	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.3	MSC – DUT Output: Normal Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.4	MSC – DUT Output: NACK Packet Response to MSC_MSG	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.5	MSC – DUT Output: Never Initiates Bad Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)

### Quantum Data MHL Compliance Test Coverage

Interface	Product Type	Section	Test Description	MHL 1.2/1.3/1.4 - QD support / Eqpt Req'd MHL 1.3/1.4 Approved by MHL Consortium!	MHL 2.0/2.1/2.2 - QD support / Eqpt Req'd Approved by MHL Consortium!
MHL	COMMON	6.3.6	MSC – DUT Output: Errors and Exceptions	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.7	DDC – DUT Input; Illegal Responses	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.8	MSC – DUT Input: Device Register Space Contents; Writes	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.9	MSC – DUT Input: Vendor Specific and Reserved Header Values	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.10	MSC – DUT Input: Normal Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.11	MSC – DUT Input: Errors and Exceptions	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.12	MSC – DUT Input: Argument Timeouts	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.13	MSC – DUT Output: Never Initiates Bad Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.14	MSC – DUT Input: Normal Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.15	MSC – DUT Output: Normal Commands	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.16	MSC – DUT Input: Errors and Exceptions	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.17	DDC – DUT Output; DUT Never Sends Illegal DDC Command	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.18	DDC – DUT Output; Normal Operation	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.19	DDC – DUT Output; Illegal Responses	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.20	DDC – DUT Input; Continuous Monitors and Normal Operation	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.21	DDC – DUT Input; Normal Operation	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
MHL	COMMON	6.3.22	DDC – DUT Input; Illegal Responses	980 CBUS Module (all tests in section)	980 CBUS Module (all tests in section)
Notes:					